1 BRS L1 5311 source) near5 USPA 2001/10/2
Capacit\$4)
2 BRS L2 3481 (pcwer adj2 mosfet) USPA 2001/10/2 0
BRS L3 29382 or silicon adj2 T 4 13:11 0
4 BRS L4 112 diffusion near3 USPA 2001/10/2 0
SPA 2001/1 4 13:2
ard 2 and 3 and 4 USPA 2 nd 5
7 BRS L7 1 1 ar.d 2 and 3 and 5 T 4 11:42 0
and 5 USPA 2
9 BRS L9 115 source)near5 US-P 2001/10/2 OF Capacit\$4) OF GPUB 4 12:39
10 BRS L10 74 (power adj2 mosfet) US-P 2001/10/2 0
9
P 2001/10/ B 4 12:39
13 BRS L12 4 diffusion near3 US-P 2001/10/2 0 (source and channel) GPUB 4 12:39
S-P 2001/10/ PUB 4 12:28
2001/10/ 4 12:29
16 BRS L17 13

0		2	2001/10/2	JРО	28 and 29 and 30 and 31 and 32	. 0	L33	BRS	32
0		20.	001/10/ 13:03	JPO	te same (trench oov\$3)	4398	L31	BRS	31
0		10	2001/10/2 4 12:47	JРО	(power adj2 mosfet)	635	L30	BRS	30
0			2001/10/2 4 12:47	JPO	poly same (insulat\$5 or silicon adj2 dioxide or oxide)	4122	L29	BRS	29
0		2	2001/10/2 4 13:04	JРО	((gate near5 source)near5 capacit\$4)	1009	L28	BRS	28
0			2001/10/2 4 12:42	EPO	20 and 21	16	L27	BRS	27
0			\	EPO	18 and 19	6	L26	BRS	26
0			0/ 1	EPO	19 and 20	5	L25	BRS	25
0			0/ 1	EPO	18 and 19 and 20	0	L24	BRS	24
0			20	EPO	18 and 19 and 20 and 21	0	L23	BRS	23
0			001/10/ 12:48	EPO	<pre>diffusion near3 (source and channel)</pre>	2	L22	BRS	22
0			001/10/ 12:48	EPO	gate same (trench or groov\$3)	1247	L21	BRS	21
0			2001/10/2 4 12:47	EPO	<pre>poly same (insulat\$5 or silicon adj2 dioxide or oxide)</pre>	1420	L20	BRS	20
0			2001/10/2 4 12:47	EPO	(power adj2 mosfet)	406	L19	BRS	19
0			2001/10/2 4 12:47	EPO	((gate near5 source)near5 capacit\$4)	280	L18	BRS	18
0			2001/10/2 4 12:33	US-P GPUB	11 and 13	58	L16	BRS	17
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0			2001/10/2	DERW	44 and 46 and diffusion	31	L54	BRS	54
0				DERW	44 and 46	161	L53	BRS	53
0			001/ 13:	DERW ENT	43 and 44 and 46	Н	L52	BRS	52
0			001/1 13:2	DERW	gate same (trench or groov\$3)	4093	L46	BRS	46
0			001/ 13:	DERW	<pre>diffusion near3 (source and channel)</pre>	13	L45	BRS	45
0			2001/10/2 4 13:12	DERW ENT	poly same (insulat\$5 or silicon adj2 dioxide or oxide)	15407	L44	BRS	44
0			2001/10/2 4 13:11	DERW	(power adj2 mosfet)	1448	L43	BRS	43
0				JPO	28 and 29	10	L42	BRS	42
0			2001/10/2 4 13:04	DERW ENT	((gate near5 source)near5 capacit\$4)	1031	L41	BRS	41
0			2001/10/2 4 13:03	JPO	gate same (poly and trench or groov\$3)	3403	L40	BRS	40
0			001/ 12:	JPO	29 and 31 and poly	84	L39	BRS	39
0			001/ 12:	JРО	29 and 31	84	L38	BRS	38
0			001/ 12:	JPO	29 and 30 and 31	0	L37	BRS	37
0			001/ 13:	JPO	28 and 29 and 30 and 31	0	L36	BRS	36
0			2001/10/2 4 12:50	JPO	29 and poly	4122	L35	BRS	35
0			001/ 12:	JPO	29 and 32	0	L34	BRS	34
0			001/1 12:4	JРО	diffusion near3 (source and channel)	11	L32	BRS	33
S T O T D	Error Definition	Comment	Time Stamp	DBs	Search Text	Hits	# #	Туре	